Syllabus

EE 2260 Fundamentals of Electric Circuits

Course Number and Title: EE 2260, Fundamentals of Electric Circuits
Credits: 4
Website: http://faculty.weber.edu/snaik/EE2260.htm

Instructor: Suketu Naik
Office: WSU Campus, ET 137
Phone: (801) 626 6895
Email: suketunaik@weber.edu (please put EE2260 in the subject line)

Office Hours:
- Ogden Campus: Mon: 12:30-1:30pm, Wed: 12:30-3:30pm
- Davis Campus: Thur: 4:00-5:00pm

Class Time and Location:
- Ogden Campus: Mon,Wed 10:30-11:45am in M2 100 (portable to the east of ET building)
- Davis Campus: Tue,Thur 5:00-8:00pm in D3-146

Lab Time and Location:
- Ogden Campus: Tue 1:30-4:30pm in ET 101A
- Davis Campus: Tue/Thur 5:00-8:00pm in D3-146


Description: Fundamental electric circuit techniques including: time domain transient responses for 1st and 2nd order circuits, Laplace transforms, Fourier series, and filters. Lecture and lab combination.

Prerequisite: EE 1270 and MATH 1220

Student Learning Outcomes: The students will demonstrate:

1. A knowledge of transient responses for 1st and 2nd order circuits.
2. An understanding of how Laplace transforms apply to circuits
3. An understanding of how Fourier series and transforms apply to signals.
4. An ability to construct simple passive and active filters.

Student Assessment:

1. Homework assignments: 10%
2. Laboratory assignments: 15%
3. 1st Midterm examination: 25%
4. 2nd Midterm examination: 25%
5. Final examination: 25%
Grades:

Grades will be assigned as follows based on the weighted average of exams, labs, and homework as shown above. Grading scale may be normalized to the highest grade in the class per the instructor’s discretion.

*Please note that C or better is required in order to obtain BSEE degree. If you fail to meet this requirement, you will have to retake the class.*

<table>
<thead>
<tr>
<th>Grade</th>
<th>Percentage</th>
<th>Grade</th>
<th>Percentage</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>&gt; 93%</td>
<td>C+</td>
<td>77-80%</td>
</tr>
<tr>
<td>A-</td>
<td>90-93%</td>
<td>C</td>
<td>73-77%</td>
</tr>
<tr>
<td>B+</td>
<td>87-90%</td>
<td>C-</td>
<td>70-73%</td>
</tr>
<tr>
<td>B</td>
<td>83-87%</td>
<td>D</td>
<td>60-70%</td>
</tr>
<tr>
<td>B-</td>
<td>80-83%</td>
<td>F</td>
<td>&lt; 60%</td>
</tr>
</tbody>
</table>

Topic Outline:

1. Review: Ch1-4, Ch9, Ch10, Ch6
2. Chapter 7: Response of First-order RL and RC Circuits
3. Chapter 8: Natural and Step Responses of RLC Circuits
4. Chapter 12: Introduction to Laplace Transform
5. Chapter 13: The Laplace Transform in Circuit Analysis
6. Chapter 16: Fourier Series
7. Chapter 17: The Fourier Transform
8. Chapter 14: Introduction to Frequency Selective Circuits
9. Chapter 15: Active Filter Circuits

Schedule:

Please refer to the class website for the tentative list of class schedule, homework problems, and the labs: [http://faculty.weber.edu/snaik/EE2260.htm](http://faculty.weber.edu/snaik/EE2260.htm)

Homework:

Homework will be assigned in class and will be due on class period after it is assigned (Example: If it is assigned on Mon 1/26, it will be due on Mon 2/2).

Late homework policy: 75% credit after 1st week, 50% after 2nd week, 25% after 3rd week, 0% after 4th week.

Homework will not be graded unless put in the proper format (see the format below) on engineering paper. It must be clear and well organized. Use only one side of each sheet. Write
your name, the class number (EE 2260) and assignment number on the top (see the format below). Do not use multiple columns; separate problems with horizontal bars and box each answer.

**Homework Grading:** Points per homework problem will be determined by the instructor. Full credit will be given for all the work and the correct answer. Homework that does not conform to the format (see the format below) will be penalized.

**Laboratory Experiments:**

Laboratory experiments will be performed periodically throughout the semester at the discretion of the instructor. You must make all requested calculations and measurements specified and record for a laboratory report. All work will be recorded in a laboratory book in the format listed below. You must assemble and write up your own report. Your report should show and include original work such as changes in the schematic or different types of related designs you tried. You will be expected to be honest in reporting your data. If you do not get the same results as expected, then you should try to understand and explain why your data was different from what you expected. You need to spend time troubleshooting your circuits, and in your report, include a little section that explains what you did and what you learned. Include and show any calculations you did. Most important is a conclusion section that ties everything together and explains what was learned. Things that cannot be read will cause you to lose points. Lab reports will be required for each lab (see the format below). For your information, lab book guidelines are also provided.

**Services for Students with Disabilities:** Any student requiring accommodations or services due to a disability must contact Services for Students with Disabilities (SSD) in Room 181 of the Student Services Center. SSD can also arrange to provide course materials (including the syllabus) in alternative formats if necessary.

**Cheating and plagiarism:** Just don’t…
## Homework Format

<table>
<thead>
<tr>
<th>Month / Day / Year</th>
<th>Course Number</th>
<th>Name (Last, First)</th>
<th>1</th>
</tr>
</thead>
</table>

### Staple

1.1 a

Complete Problem Definition. Including figures, graphs, schematics, etc. May be copied directly from the assignment.

Leave some space between definition and solution

Show Problem Solution.

**SHOW ALL WORK**

Separate parts with one line

Separate problems with a double line.

1.1 b

Repeat the above format for all remaining problems.

### NOTES:

1. Use only one side of engineering paper [E. 2].
2. Staple multiple pages.
3. Number all pages (page # / of #).
4. You can work more than one problem per page if space is available.
5. Use PENCIL and eraser.
6. PRINT, no script. All printing must be neat and horizontal.
7. Each problem definition should have all pertinent information required to understand the problem without referring to the textbook.
8. Organize your solution so that it can be easily followed.
Lab Report Format (make sure to write your name)

<table>
<thead>
<tr>
<th>Title</th>
<th>Introduction to Lab Books (Title should be at the top of every page and referenced in the Table of Content).</th>
</tr>
</thead>
<tbody>
<tr>
<td>Purpose:</td>
<td>Purpose statements should be short descriptions of what you will be doing on the following pages. It may be cut and pasted directly out of the lab handout. (i.e. The purpose of this lab is to layout the lab book format.)</td>
</tr>
<tr>
<td>Preliminary:</td>
<td>Should follow the sequence of the lab handout with all subsections being recorded in an outline type format. Subsections must include a description of all subsequence calculations. Subsections descriptions may be cut and pasted directly from the lab handout.</td>
</tr>
<tr>
<td>1.</td>
<td>This section should be done prior to the scheduled lab time.</td>
</tr>
<tr>
<td>2.</td>
<td>Do Not just write the equations, there must be a verbal description of what the equations are trying to accomplish.</td>
</tr>
<tr>
<td>3.</td>
<td>Show All Work</td>
</tr>
<tr>
<td>4.</td>
<td>All Graphs should have a short caption and have all axis labeled.</td>
</tr>
<tr>
<td>5.</td>
<td><strong>Box Important Information</strong></td>
</tr>
<tr>
<td>6.</td>
<td>Should be done in BLUE OR BLACK PEN</td>
</tr>
<tr>
<td>7.</td>
<td>Strike out or Cross out all errors (DO NOT ERASE)</td>
</tr>
<tr>
<td>8.</td>
<td>Any blank area must be designated by having an 'X' or 'Page left blank' written in.</td>
</tr>
<tr>
<td>9.</td>
<td>Should be done in order. See advise via email or face to face if a section does not make sense. If you jump around you will not be prepared for the following subsections.</td>
</tr>
<tr>
<td>10.</td>
<td><strong>Must be completed prior to any actual circuit construction.</strong></td>
</tr>
</tbody>
</table>

**Procedure:** Should follow the format of the preliminary but include all actual measurements, and the condition/settings of the test equipment, performed in the lab.

**Conclusion:** Should be at least ¼ a page in length and include answers to any question called out directly in the lab. It should also summarize all of your findings in a common table and compare the designed and measured values and any

\[
\%\text{error} = \left| \frac{\text{Measured Value} - \text{Calculated Value}}{\text{Calculated Value}} \right| \times 100\%
\]

with an explanation for all discrepancies over 10%.

*Sign and date each page and at the end of the lab*
Lab Book Guidelines

1. Keeping a proper lab book is essential to establish ownership of intellectual property (which is the primary output of all engineering). Try to follow these guidelines outside of this class as a good practice to maintain lab books.

2. Start each lab at the top of a new page. For each lab, write your name, lab number, and the title at the top of the page. See the format below.

3. Use the first page in your lab book as table of contents. Each time you start a new lab, add an entry with its title, date and page number. See example below:

<table>
<thead>
<tr>
<th>Lab #</th>
<th>Title</th>
<th>Date</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Intro to Matlab</td>
<td>1/21/2014</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>RC Circuits</td>
<td>1/28/2014</td>
<td>10</td>
</tr>
<tr>
<td>...</td>
<td>...</td>
<td>...</td>
<td>...</td>
</tr>
</tbody>
</table>

4. Conclude each lab with a summary or conclusion that briefly describes how the circuit/code/etc performed. Summary or conclusion should mention the problems you encountered and if/how you addressed it.

5. Don’t use three ring binders or spiral bound notebooks. Use a proper bound Lab book.

6. Number all pages! Number them by hand if necessary.

7. Printed work can be included in the book. Make sure to tape it neatly or glue it properly.

8. Sign or initial and date each page. If a page contains work from different dates, it should be separated with horizontal lines and each section (from different dates) should be signed or initialed and dated.

9. Don’t leave blank pages. If you don’t want to use a page, then draw a diagonal line from one corner to the other. Initial and date the blank page.

10. Do not a) overwrite something, b) scribble out something, or c) use white-out to cover something. If you make small errors, STRIKE them out with single lines. If you make large set of errors, use diagonal lines. If the strikeout occurs on a different day (e.g. two weeks from the day you wrote something), it should also be initialed and dated.

11. Use your lab book! Make it your work book. Do not write on separate papers or sheets or sticky notes. Try not to transfer notes from scratch paper into the lab book.